

APPLN. FILING DATE: FEBRUARY 14, 2002
TIME: DATA PROCESSING ARCHITECTURES

INVENTOR(S): JOHN RHOADES ET AL. APPLICATION SERIAL NO: 10/073,948

SHEET 1 of 9

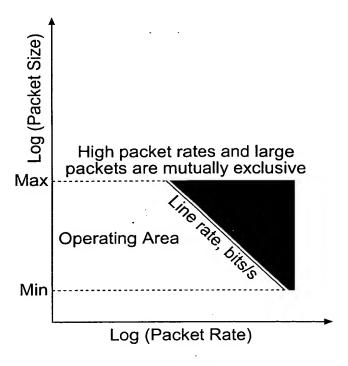


Fig. 1

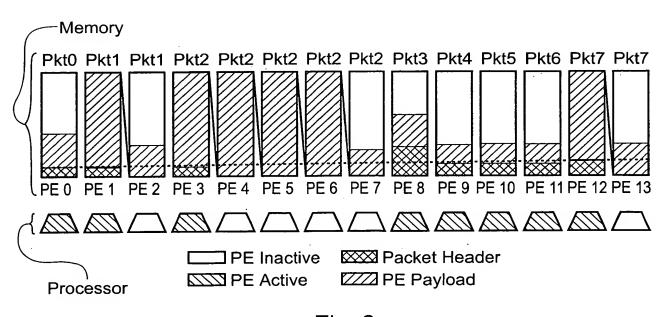
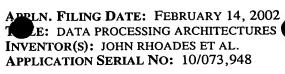
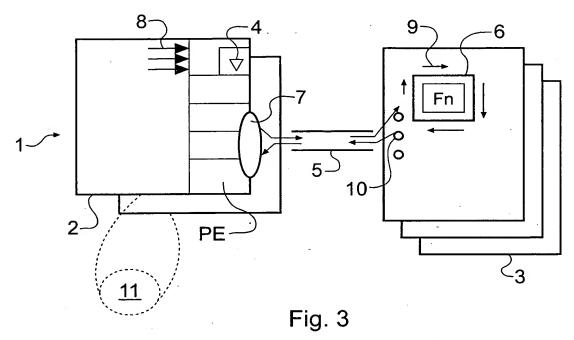


Fig. 2









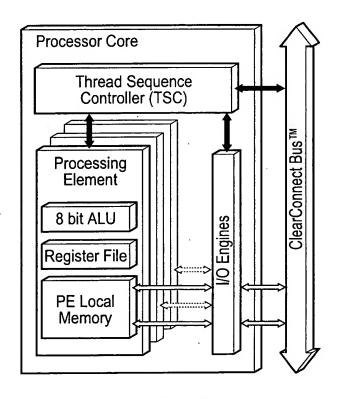


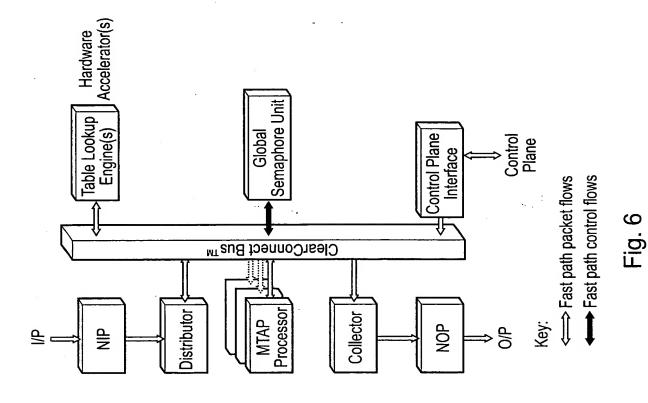
Fig. 4

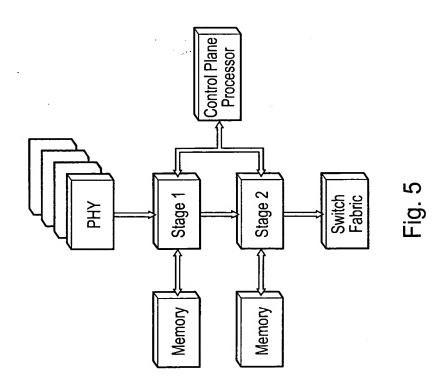


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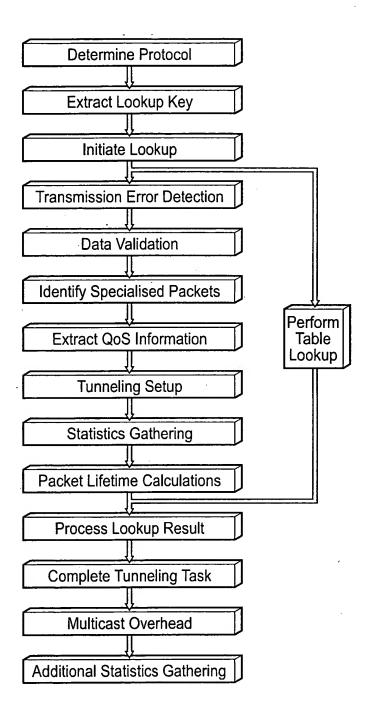


Fig. 8

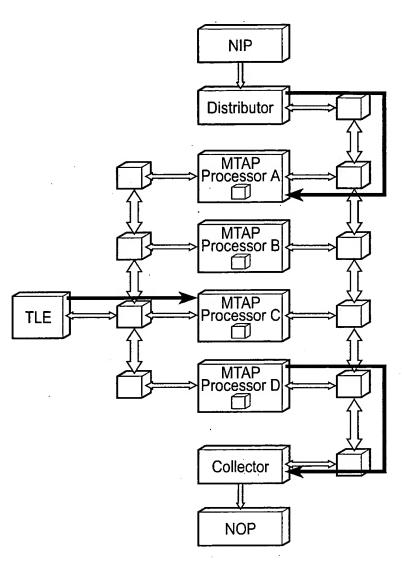
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Key:

Segment of high-speed on-chip network

High-speed on-chip network interface

Set of packets being processed

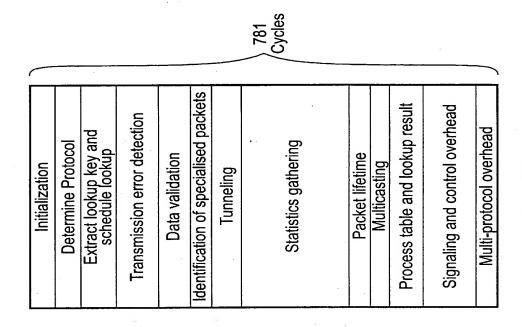
Fig. 9

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Further additional processing time when supporting MPLS apporting MPLS from Supporting IPv6 supporting IPv6 supporting IPv6 Packet processing time when supporting IPv4 only IPv4 only Pig. 10

Fig. 11



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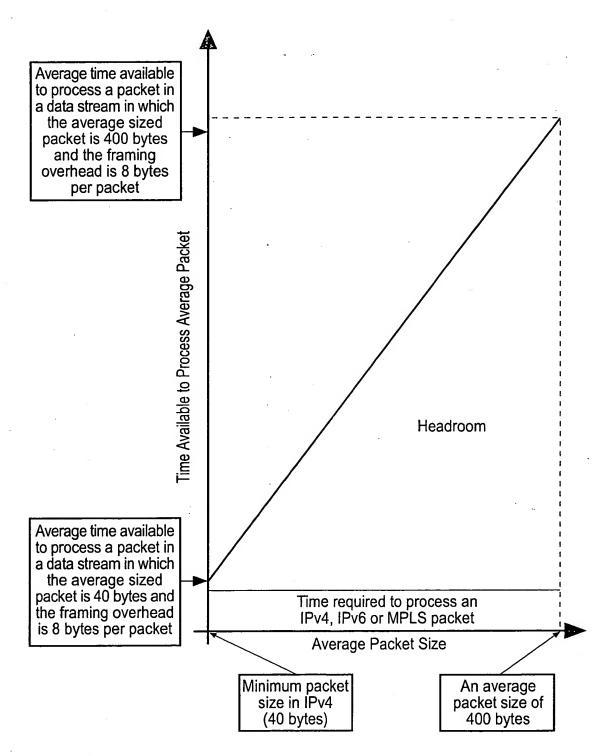
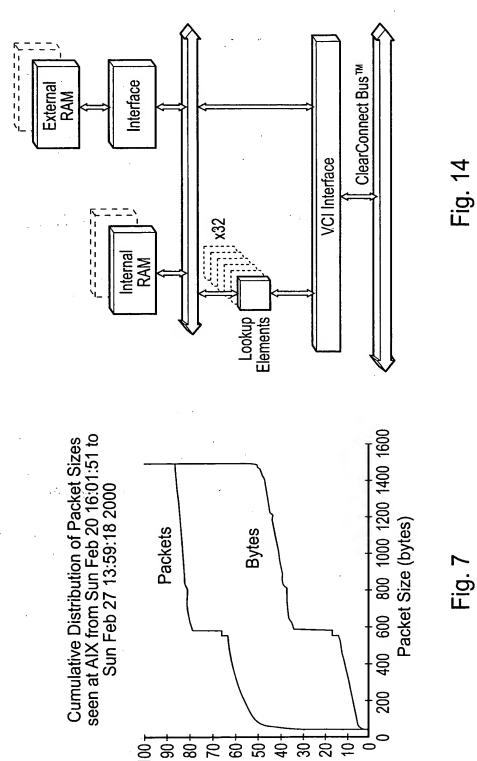


Fig. 12

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Cumulative Percentage

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